PTO/SB/21 - MODIFIED
09/2004

TRANSMITTAL FORM

to be used for all correspondence after initial filing

* Plus 7 cited references

Application Number	10/786,010
Filing Date	02/26/2004
First Named Inventor	Pierte ROO
Group Art Unit	2682
Examiner Name	Eugene Yun

Total number of pages in this submission including transmittal	4 *	
Attorney Docket Number	MP0039.C1	

ENCLOSURES   *(check all that apply)*

- [ ] Fee Transmittal Form
  - [ ] Fee Attached - Check
- [ ] Amendment / Reply
  - [ ] After Final
  - [ ] Affidavits/declaration(s)
- [ ] Extension of Time Request
- [ ] Express Abandonment Request
- [x] Information Disclosure Statement
  with PTO 1449 and 7 cited references
- [ ] Certified Copy of Priority Document(s)
- [ ] Response to Missing Parts/ Incomplete Application
  - [ ] Response to Missing Parts under 37 CFR 1.52 or 1.53

- [ ] Assignment Papers *(for an Application)*
- [ ] Drawing(s)
- [ ] Licensing-related Papers
- [ ] Petition
- [ ] Petition to Convert to a Provisional Application
- [ ] Power of Attorney, Revocation Change of Correspondence Address
- [ ] Terminal Disclaimer
- [ ] Request for Refund
- [ ] CD, Number of CD(s) _____
  - [ ] Landscape Table on CD

- [ ] After Allowance Communication to TC
- [ ] Appeal Communication to Board of Appeals and Interferences
- [ ] Appeal Communication to Group *(Appeal Notice, Brief, Reply Brief)*
- [ ] Proprietary Information
- [ ] Status Letter
- [ ] Other Enclosure(s) (identify below):

REMARKS

SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT

Firm Name	KATTEN MUCHIN ROSENMAN, LLP
Signature	*Andrew J. Bateman*
Printed Name	Andrew J. Bateman
Reg. No.	45,573
Date:	10/03/2005

CERTIFICATE OF FACSIMILE TRANSMISSION OR MAILING

I hereby certify that this correspondence is being facsimile transmitted to the USPTO via fax no. (703) 872-9306 or is being deposited with the US Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on the date shown below:

Signature	
Typed or Printed Name	
	Date:

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:	)
	) Examiner: Eugene Yun
Pierte ROO	)
	) Group Art Unit: 2682
Application No.: 10/786,010	)
	) Confirmation No.: 3556
Filed: February 26, 2004	)
	)
For: ACTIVE RESISTIVE SUMMER FOR	)
A TRANSFORMER HYBRID	) Date: October 3, 2005

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In compliance with the duty of disclosure under 37 CFR § 1.56 and the requirements of MPEP § 2001.06(c), and in accordance with the practice under 37 CFR §§ 1.97 and 1.98, the Examiner's attention is directed to the documents listed on the enclosed PTO-1449s and to the attached copies of any non-U.S. Patent or literature references cited thereon.

In accordance with 37 CFR § 1.97(h), this Information Disclosure Statement is not to be construed as an admission that the information cited is or is considered to be material to patentability as defined in 37 CFR § 1.56(b), nor as an admission that the information constitutes prior art within the meaning of 35 USC §§ 102 and/or 103.

It is respectfully requested that the information listed on the PTO-1449 be considered by the Examiner, and that an initialed copy of the PTO-1449 be returned indicating that such information was considered.

No fee is believed necessary for the submission of this Information Disclosure Statement. However, if deemed necessary, the Commissioner is authorized to charge the IDS fee of $180.00 to Deposit Account No. 50-1710.

Should the Examiner have any questions, Applicant's undersigned attorney is reachable by telephone in our Washington, D.C. office at (202) 625-3547. The correspondence address of record is provided below.

Respectfully submitted,

IP Docket
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FORM PTO 1449 MODIFIED	ATTORNEY DOCKET NO.	APPLICATION NO.
U.S. PATENT AND TRADEMARK OFFICE	MP0039.C1	10/786,010

LIST OF REFERENCES CITED BY APPLICANT	APPLICANT	
	Pierte ROO	

DATE SUBMITTED TO USPTO: October 3, 2005	FILING DATE	GROUP
	02/26/2004	2682

OTHER DOCUMENTS (Including author, title, date, pertinent pages, etc.)

*EXAMINER INITIALS		
		Dunning, Jim, "An All-Digital Phase-Locked Loop with 50-Cycle Lock Time Suitable for High-Performance Microprocessors," IEEE JOURNAL OF SOLID-STATE CIRCUITS, Vol. 30, No. 4, April 1995, pp. 412-422
		Maneatis, John G., "FA 8.1: Low-Jitter and Process-Independent DLL and PLL Based on Self-Biased Techniques"
		Moon, Yongsam, et al., "An All-Analog Multiphase Delay-Locked Loop Using a Replica Delay Line for Wide-Range Operation and Low-Jitter Performance," IEEE JOURNAL OF SOLID-STATE CIRCUITS, Vol. 35, No. 3, March 2000, pp. 377-384
		Wang, HongMo, "WP 23.8: A 9.8GHz Back-Gate Tuned VCO in 0.35µm CMOS," ISSCC99, Session 23, Paper WP 23.8, 1999 IEEE INTERNATIONAL SOLID-STATE CIRCUITS CONFERENCE, pp. 406-407 & 484
		Rudell, et al., "SA 18.3: A 1.9 GHz Wide-Bank IF Double Conversion CMOS Integrated Receiver for Cordless Telephone Applications, " 1997, pp. 304-305, 476
		Dally, William J., et al., "High-Performance Electrical Signaling," Publication and date unknown

EXAMINER	DATE CONSIDERED

* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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LIST OF REFERENCES CITED BY APPLICANT

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APPLICANT	
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FILING DATE	GROUP
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U.S. PATENT DOCUMENTS

*EXAMINER INITIALS		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE

FOREIGN PATENT DOCUMENTS

*EXAMINER INITIALS		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION OR ABSTRACT (Yes or No)
		WO 00/28663 A3	05/18/2000	PCT			

OTHER DOCUMENTS (Including author, title, date, pertinent pages, etc.)

EXAMINER	DATE CONSIDERED

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